Development Tendency of Sliding Mode Frequency Shift Type Anti-Islanding of Japanese Companies

Ji-Won Jeong*, Dong-Hee Kim* and Byoung-Kuk Lee*

Abstract – As tightened the grid connection related regulation for a distributed power generation Japan, islanding of the distributed power generation should be detected more rapidly. In this paper, it is introduced the development tendency of sliding mode frequency shift type anti-islanding technology in Japanese companies.

Keywords: Distributed power generation, Anti-islanding, Sliding mode frequency shift

1. Introduction

According to exhaustion of fossil fuels and global warming, researches and installations of renewable energy sources have been increased [1-3]. This generation systems are connected to a conventional electric grid, which systems compose to distributed generation (DG) system [4]. According to DG composition, standards and guide for DG have been set in order to prevent accident.

In Japan, an investment of renewable energy sources, which are composed to DG, has been increased much due to an atomic accident in 2011. These DGs have been installed by obeying standards and guides of Japan. However, Japanese standards and guides are tightened especially the grid connection. Islanding of DGs occurs when each DGs continue to operate and energize the grid line during the grid fault status. This islanding, also called unintended islanding, causes a hazard to the grid and electric equipment, maintenance personnel, and etc. Therefore, in order to prevent these problems, the antiislanding algorithm should be adopted to DGs. As consolidated from 'Old Guide Line' to 'Grid Connection Regulation for the distributed power generation' in Japan on June 2006 [5], it is required to be detected islanding of the distributed power generation in 100ms and to be retained power factor up to 95% during the detecting time.

This paper presents and discusses developed antiislanding methods in order to meet the consolidated standard for grid connection.

2. Developed Anti-islanding Methods

2.1 The Needs of Anti-islanding

The distribution of electric power from utility companies to customers utilizes a network of utility lines connected in an electrical grid. The electrical grid consists of the main grid and many independent energy sources which supplies the electric energy to the grid. Each DG includes energy sources, such as photovoltaics, micro-turbines, fuel cells, wind, and engine-generator sets, and the power conditioning system (PCS) which includes a converter and an inverter, as shown in Fig. 1. Also, the energy storage system, such as the battery and flywheel, can be added. A typical feeder system consists of distribution lines that provide power from the grid to a customer load and the DGs via electrical disconnects and distribution transformers. Even with the presence of a DG connected to the grid, the utility company is still the main source of power and in many cases controls the system voltage and frequency within nominal values.

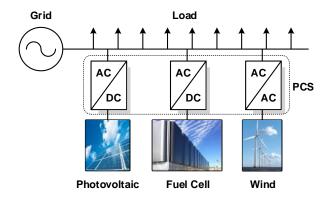


Fig. 1. Block diagram of DPGS and power grid

^{*} College of Information & Communication Engineering, SungKyunKwan University, Korea. (jjw@innolow.co.kr)

Under certain conditions, each distributed power generation system is disconnected from the grid and feeder system from the grid fault, leaving the DG directly tied to the load or disjointed grid branch, which is referred to as islanding. The isolated section of the grid being powered by the DG is referred to as an island. Unintentional islanding results in a situation where the voltages and frequencies on the faulted grid branch are outside of the direct control of the utility company because branch is primarily energized by one or more DG. Accordingly, monitoring and disconnecting schemes, referred to as anti-islanding schemes, are used to timely disconnect a DG from the feeder in the event that grid power from a utility company has been disconnected from the feeder.

Especially, IEEE Std. 929-2000 defines anti-islanding as "A condition in which a portion of the utility system that contains both load and distributed resources remains energized while isolated from the remainder of the utility system."

2.2 Kind of Anti-islanding Schemes

Recently, anti-islanding schemes have been commercialized and proposed in Japan include mainly active schemes or hybrid schemes.

Active schemes are based on active signal injection with monitoring of the resulting grid signals, such as impedance measurement for instance, or active signal injection with active controls, such as active frequency drift, Sandria frequency shift, harmonics injection or reactive power variation. Hybrid schemes are a combination of active schemes and passive schemes.

With active schemes and hybrid schemes, some distortion would occur in the output current waveform of the inverter, thereby resulting in a tradeoff between islanding detection time and waveform distortion, i.e, the faster detection typically results in higher total harmonic distortion (THD).

3. Trends of Anti-Islanding Schemes

Japanese companies which develop and commercialize an anti-islanding schemes using active frequency shift, are Omron, Toshiba, Fuji Electric, Mitsubishi, Tokyo Electric Power, Panasonic, Denso, and Daihen etc..

Omron has brought a remarkable development during the second half of the 2000s to meet the consolidated standard for the grid connection in 2006. On the other hand, Fuji Electric has done a poor development activity during the 2000s while it had done a rich development activity related

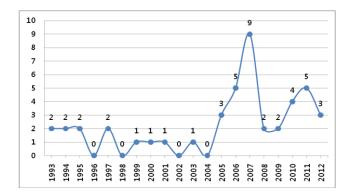


Fig. 2. Applications per year

to the anti-islanding schemes during the second half of the 1990s. And Toshiba has acted a steady development activity since 1993. There has been filed the most patent applications in Japanese Patent Office for 2 years, 2006 and 2007, since 1993, as shown in Fig. 2 [6-42].

3.1 Patent Appl. No. 1993-023894 (Toshiba Corp.)

When an imbalance between the reactive power of the inverter output and the reactive power of the load occurs, the frequency is varied. In the case of increased frequency, the inverter current phase is led so as to cause the frequency to rise still further. While, when the frequency has fallen, the current phase of the inverter is delayed, and the divergence in the direction to lower the frequency is affected. This positive feedback leads to the imbalance between the output of the inverter and the load rapidly. In the end, the islanding of the distributed power generation can be detected rapidly.

However, there is a shortcoming that the grid-connected inverter with an excessive positive feedback in this scheme cannot retain power factor up to 95%.

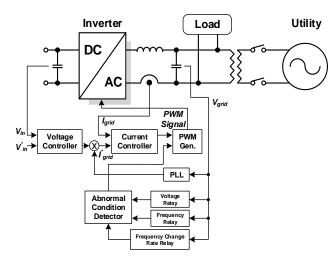


Fig. 3. Anti-islanding scheme in Patent Appl. No. 1993-023894

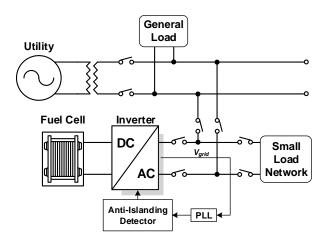


Fig. 4. Anti-islanding scheme in Patent Appl. No. 1994-201608

3.2 Patent Appl. No. 1994-201608 (Fuji Electric)

A grid-connected inverter controls the reactive power to have a very small periodic fluctuation continuously, monitoring whether a change of a frequency of the grid-connected inverter exists, integrating a deviation between the monitored frequency and the reference frequency, and determining the present state an islanding when an integration value of the frequency deviation is larger than the pre-determined value.

However, there is a disadvantage that the grid-connected inverter having the periodic fluctuation on the reactive power leads harmonics on the grid frequency and degrades the power factor.

3.3 Patent Appl. No. 2003-349487 (Omron Corp.)

A single operation detector of the distributed power source detects a single operation of the distributed power source when the AC system is power interrupted during interconnecting operation of the distributed power source and the AC system which transfers a power from the distributed power source to the AC system through a threephase AC power source. The single operation detector includes a CPU for controlling an inverter so that a power is extracted from one phase (S-T phase) of three phases (R-S phase, S-T phase and R-T phase) of the three-phase AC power source, and a frequency change detector for detecting a frequency change based on the reactive power change from the power inputted by the reactive power change. The CPU judges whether this frequency change is a predetermined level or not when the frequency change is detected, and detects the single operating of the distributed power source when the predetermined level or higher is judged.

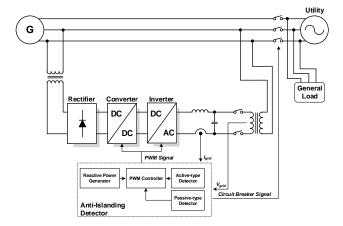


Fig. 5. Anti-islanding scheme in Patent Appl. No. 2003-349487

This scheme is advantage that a number of component as well as the influence of an input of an active change on an AC system is decreased because the single operation detector uses just one phase of three phases.

3.4 Patent Appl. No. 2007-168421 (Omron Corp.)

In the islanding detection method, the reactive power for detecting whether or not the distributed power supply is disconnected from the power system and is islanded is injected into the power system. In this method, the reactive power is injected into the power system when the system frequency has not substantially changed over multiple system periods in the past and the system voltage fluctuated. It is preferably determined that the system frequency has not substantially changed when the system frequency deviation has been continuously kept within a certain range over the multiple system periods in the past. It is preferably

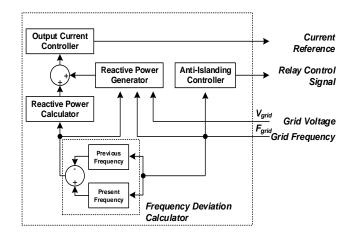


Fig. 6. Anti-islanding scheme in Patent Appl. No. 2007-168421

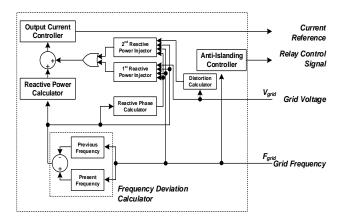


Fig. 7. Anti-islanding scheme in Patent Appl. No. 2007-

determined that the system voltage has fluctuated when the system voltage in each system period changes with a fluctuation margin specified for each system period relative to the average value of the respective system voltages of the multiple system periods in the past.

The scheme of Fig. 6 is advantageous in the case of the detection of anti-islanding when injected reactive power is balanced with load reactive power.

3.5 Patent Appl. No. 2007-309908 (Omron Corp.)

In the case in which an additional injected reactive power is injected for a predetermined period when injected reactive power is balanced with the reactive power of the load. This islanding detecting method includes: determining that additional injection reactive power is injected at a phase corresponding to the system frequency deviation (initial determination); re-determining the phase of the additional injection reactive power based on the code of the system frequency deviation after a period shorter than the above period elapses from the additional reactive power injection; and newly injecting the additional injection reactive power of the phase corresponding to the code of the system frequency deviation for a predetermined period, if the phase of the additional injection reactive power from the code of the system frequency deviation when redetermined is reverse to the phase of the additional injection reactive power when initially determined or and continuing the injection of the additional injection reactive power at the same phase for residual period until a predetermined period elapses from the additional injection reactive power injection at the initial determination, if the phase is the same.

The scheme of Fig. 7 is much more advantageous in the case of the detection of anti-islanding when injected reactive power is balanced with load reactive power.

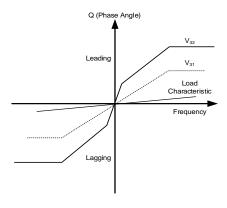


Fig. 8. Frequency change rate of inverter in Patent Appl. No. 2008-287930

3.6 Patent Appl. No. 2008-287930 (Toshiba Corp.)

The individual operation detection device of an inverter converts DC power to AC power and operates while being systematically connected to an AC power system. The device includes a drive means which performs reactive power control on the output of the inverter while synchronizing with the voltage phases of the AC power system and an output of the inverter and exerting control to a predetermined current phase and which performs control so that the reactive power of the inverter changes based on a frequency (f) and a frequency change rate (df/dt) of the output voltage of the inverter when the inverters is separated from the AC power system. The drive means makes control in such a direction that the frequency or frequency change rate of the output voltage of the inverter promotes the change by positive feedback.

There is a merit that this scheme can shorten the detecting time of anti-islanding and retain power factor up to 95% during the detecting time.

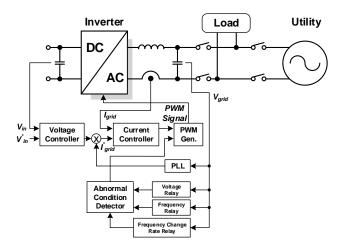


Fig. 9. Anti-islanding scheme in Patent Appl. No. 2010-053522

3.7 Patent Appl. No. 2010-053522 (Toshiba Corp.)

The inverter of the system is operated in a manner such that if the frequency deviation is positive, the phase of the reactive power of the load is controlled to be leading. If the frequency deviation is negative, the phase of the reactive power of the load is controlled to be lagging. Moreover, when the frequency change is in normal state, the inverter is controlled to move the characteristic curve of the reactive power over the frequency into the point which the reactive power is nearly zero, in order to follow the frequency change so slowly. Whereas, when the frequency change is large in abnormal state, the inverter is controlled to be the positive feedback in order to aggravate the change rate of the frequency.

We think that the inverter of this system can meet the grid connection related regulation which is required to be detected islanding of the distributed power generation in 100ms and to be retained power factor up to 95% during the detecting time.

3.8 Patent Appl. No. 2010-185488 (Toshiba Corp.)

The inverter of the system controls the output power of the inverter by adding the output of the first function circuit and the output of the second function circuit. The first function circuit is related to the phase of the reactive power of the load which is controlled to be leading when the frequency is positive. And the phase of the reactive power of the load is controlled to be lagging when the frequency deviation is negative. The second function circuit is related the phase of the reactive power of the load controlled to be leading if the frequency deviation is positive, and the phase

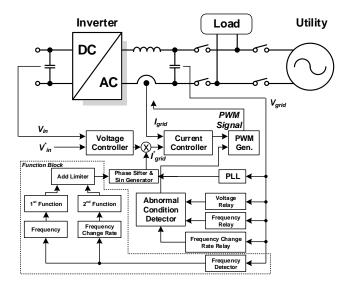


Fig. 10. Anti-islanding scheme in Patent Appl. No. 2010-185488

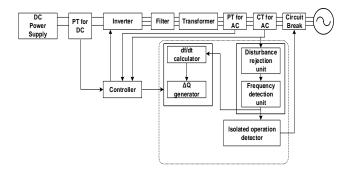


Fig. 11. Anti-islanding scheme in Patent Appl. No. 2011-243276

of the reactive power of the load which is controlled to be lagging if the frequency deviation is negative. Specially, the inverter of the system increases the gain of the second function circuit when the third, the fifth or the seventh harmonics output voltage is higher than the predetermined value.

There is an advantage that this control scheme can suppress the change of the reactive power in a normal state and increase the gain of the positive feedback of the frequency in an abnormal state to detect islanding shortly.

3.9 Patent Appl. No. 2011-243276 (Daihen Corp.)

An anti-islanding detection apparatus 7 inputs the fluctuation control signal to the control device 6. The amplitude of the fluctuation control signal is varied proportional to the rate of change of frequency generated by fluctuation control signal generator 71. The apparatus 7 increases an amplitude variation of an inverter output voltage as the rate of change of frequency increases. An unbalanced component included in the three-phase output voltage of the inverter 2 detected by the AC output voltage detector and harmonic components are removed by the band-pass filter of the complex coefficient. The apparatus 7 detects the frequency by using only the fundamental wave component outputted from the band-pass filter of the complex coefficient. If the frequency variation exceeds a predetermined threshold, the apparatus judges the inverter 2 being in the islanding state.

There is a merit that this scheme can shorten the detecting time of anti-islanding by using only the frequency of the fundamental wave component.

4. Conclusion and Future Works

In order to satisfy the consolidated grid connection related regulation for a distributed power generation, Japanese companies have developed the various methods using the positive feedback more precisely.

Since 2006, Japanese companies have commercialized or proposed anti-islanding schemes which vary to promote the change of the gain itself such as the change rate of the grid frequency in a specific period to meet the regulation relating to power factor. And other schemes detect the islanding state shortly by way of injecting of the additional reactive power when the injected reactive power is balanced with the load reactive power.

However, these schemes have the periodic fluctuation on the reactive power which leads harmonics on the grid frequency and degrades the power factor.

Hereafter, there is a need to include non-detection zone in these anti-islanding schemes to meet the consolidated standard for grid connection.

References

- [1] Y. W. Li and C. N. Kao, "An accurate power control strategy for powerelectronics-interfaced distributed generation units operating in a low voltage multi bus micro grid," *IEEE Trans. Power Electron.*, vol. 24, no. 12,pp. 2977–2988, Dec. 2009.
- [2] H. Kakigano, Y. Miura, T. Ise, and R. Uchida, "DC micro-grid for super high quality distribution-system configuration and control of distributed generations and energy storage devices," *in Proc. IEEE Power Electron .Spec. Conf. (PESC'06)*, pp. 1–7.
- [3] M. Korpaas, A. T. Holen, and R. Hildrum, "Operation and sizing of energy storage for wind power plants in a market system," *Int. J. Electr . Power Energy Syst.*, vol. 25, no. 8, pp. 599–606, Oct. 2003.
- [4] J.M. Guerrero, F. Blaabjerg, T. Zhelev, K. Hemmes, E. Monmasson, S. Jemei, M. P. Comech, R. Granadino, and J. I. Frau, "Distributed generation: Toward a new energy paradigm," *IEEE Ind. Electron. Mag.*, vol. 4, no. 1,pp. 52–64, Mar. 2010.
- [5] Standard for Interconnection of Power System Regulation, *JEAC 9701-*2006.
- [6] Toshiba, Japanese Patent 1993-023894, Feb. 12, 1993.
- [7] Toshiba, Japanese Patent 1993-262844, Oct. 21, 1993.
- [8] Fuji Electric, Japanese Patent 1994-201608, Aug. 26, 1994.
- [9] Fuji Electric, Japanese Patent 1994-268506, Nov. 1, 1994.
- [10] Fuji Electric, Japanese Patent 1995-164966, Jun.30, 1995.
- [11] Fuji Electric, Japanese Patent 1995-173368, Jun. 10, 1995.
- [12] Fuji Electric, Japanese Patent 1997-054329, Mar. 10. 1997.
- [13] Fuji Electric, Japanese Patent 1997-210310, Aug.5, 1997.
- [14] Mithsubishi Electric, Japanese Patent 1999-136355, May17, 1999.

- [15] Mithsubishi Electric, Japanese Patent 2000-076236, Mar. 17, 2000.
- [16] Mithsubishi Electric, Japanese Patent 2001-054341, Feb. 28, 2001.
- [17] Toshiba, Japanese Patent 2002-219677, Jul. 29, 2002.
- [18] Omron, Japanese Patent 2003-349487, Oct. 08, 2003.
- [19] Omron, Japanese Patent 2005-073103, Mar. 15, 2003.
- [20] Mithsubishi Electric, Japanese Patent 2005-220080, July. 29, 2005.
- [21] Omron, Japanese Patent 2007-006095, Jan. 15, 2006.
- [22] Omron, Japanese Patent 2006-016296, Jan. 25, 2006.
- [23] Omron, Japanese Patent 2006-225016, Aug. 22. 2006.
- [24] Omron, Japanese Patent 2006-226762, Aug. 23, 2006.
- [25] Omron, Japanese Patent 2006-234235, Aug. 30, 2006.
- [26] Omron, Japanese Patent 2006-234339, Aug. 30, 2006.
- [27] Omron, Japanese Patent 2006-235572, Aug. 31, 2006.
- [28] Toshiba, Japanese Patent 2006-254676, Sep. 20, 2006.
- [29] Tokyo Electric Power, Japanese Patent 2007-005918, Jan. 15, 2007.
- [30] Omron, Japanese Patent 2007-005945, Jan. 15, 2007.
- [31] Omron, Japanese Patent 2007-014049, Jan. 24, 2007.
- [32] Omron, Japanese Patent 2007-168421, Jun. 27, 2007.
- [33] Omron, Japanese Patent 2007-026463, Feb. 6, 2007.
- [34] Panasonic, Japanese Patent 2007-206539, Aug. 08, 2007.
- [35] Omron, Japanese Patent 2007-209285, Aug. 10, 2007.
- [36] Omron, Japanese Patent 2007-305290, Nov. 27, 2007.
- [37] Omron, Japanese Patent 2007-309908, Nov. 30, 2007.
- [38] Omron, Japanese Patent 2007-309907, Nov. 30, 2007.
- [39] Toshiba, Japanese Patent 2008-287930, Nov. 10, 2008.
- [40] Omron, Japanese Patent 2008-318689, Nov. 15, 2008.
- [41] Toshiba, Japanese Patent 2010-053522, Mar. 10, 2010.
- [42] Toshiba, Japanese Patent 2010-185488, Aug. 20, 2010.
- [43] Takagi, Japanese Patent 2011-176356, Aug. 21, 2011.
- [44] Daihen, Japanese Patent 2011-243276, Nov. 7, 2011.
- [45] Toshiba, Japanese Patent 2012-135652, Jun. 15, 2012.



Ji-Won Jung received the B.S. and the M.S. degrees from Hanyang University, Seoul, Korea, 1992 and 1994. Since 2009, he has worked for his Ph.D in Graduate School of Photovoltaic System Engineering, Sungkyunkwan University, Suwon, Korea. His research

interests are control algorithm for emerging energy systems.



Dong-Hee Kim received the B.S. and the M.S. degrees from Sungkyunkwan University, Suwon, Korea, in 2009 and 2011, respectively. Since 2011, he has worked for his Ph.D in Electrical Engineering at Sungkyunkwan University. His research interests include PCS and DC-DC converter for renewable energy and battery charger for

HEVs/EVs.



Byoung-Kuk Lee received the B.S. and the M.S. degrees from Hanyang University, Seoul, Korea, in 1994 and 1996, respectively and the Ph.D. degree from Texas A&M University, College Station, TX, in 2001, all in electrical engineering. From 2003 to

2005, he has been a Senior Researcher at Power Electronics Group, Korea Electrotechnology Research Institute (KERI), Changwon, Korea. From 2006 Dr. Lee joins at School of Information and Communication Engineering, Sungkyunkwan University, Suwon, Korea. His research interests include charger for electric vehicles, hybrid renewable energy systems, dc distribution systems for home appliances, power conditioning systems for fuel cells and photovoltaic, modeling and simulation, and power electronics.

Prof. Lee is a recipient of Outstanding Scientists of the 21st Century from IBC and listed on 2008 Ed. of Who's Who in America. Prof. Lee is an Associate Editor in the IEEE Transactions on Industrial Electronics and Power Electronics. He was the General Chair for IEEE Vehicular Power and Propulsion Conference (VPPC) in 2012.