



「 7 」

ETRI . . . ,



「 7 」

DMB

ETRI()

12 1 2

“ 가

가 12

A/D

ETRI

“ 3

5-mW 0.26-mm² 10-bit 20-MS/s CMOS

ADC”

ADC(A/D)



5 가 7 3 ETRI 가 63 가 ()

7

(1)				3 5-mW 0.26-mm ² 10-bit 20-MS/s CMOS ADC
(1)				
(2)				DMB 10b 25MS/s 4.8mW 0.13um CMOS A/D
				AMOLED
(5)				
			()	(Ultra Low Dropout) Regulator
				SONET OC-192 10Gb/s CMOS CDR/DEMUX IC
				2.4GHz ZigBee (IEEE802.15.4) CMOS
				Error Vector Magnitude (EVM)
(3)				A 1.65Gb/s/ch CMOS Semi-Digital Data Recovery with 2x Converse Oversampling
				A Dual-Channel 6b 1GS/s 0.18um CMOS ADC for Ultra Wide-Band Communication Systems
			()	Noise 가 Wide-Range PLL



go.kr)

([http://www.kipo.](http://www.kipo.go.kr)

가

가

가

「 」

1

7 1

가

1

(FTA)

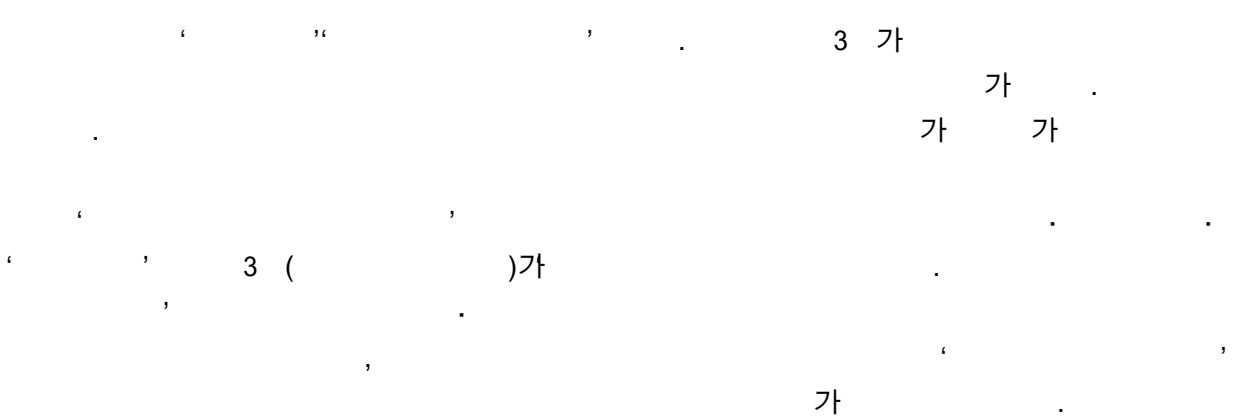
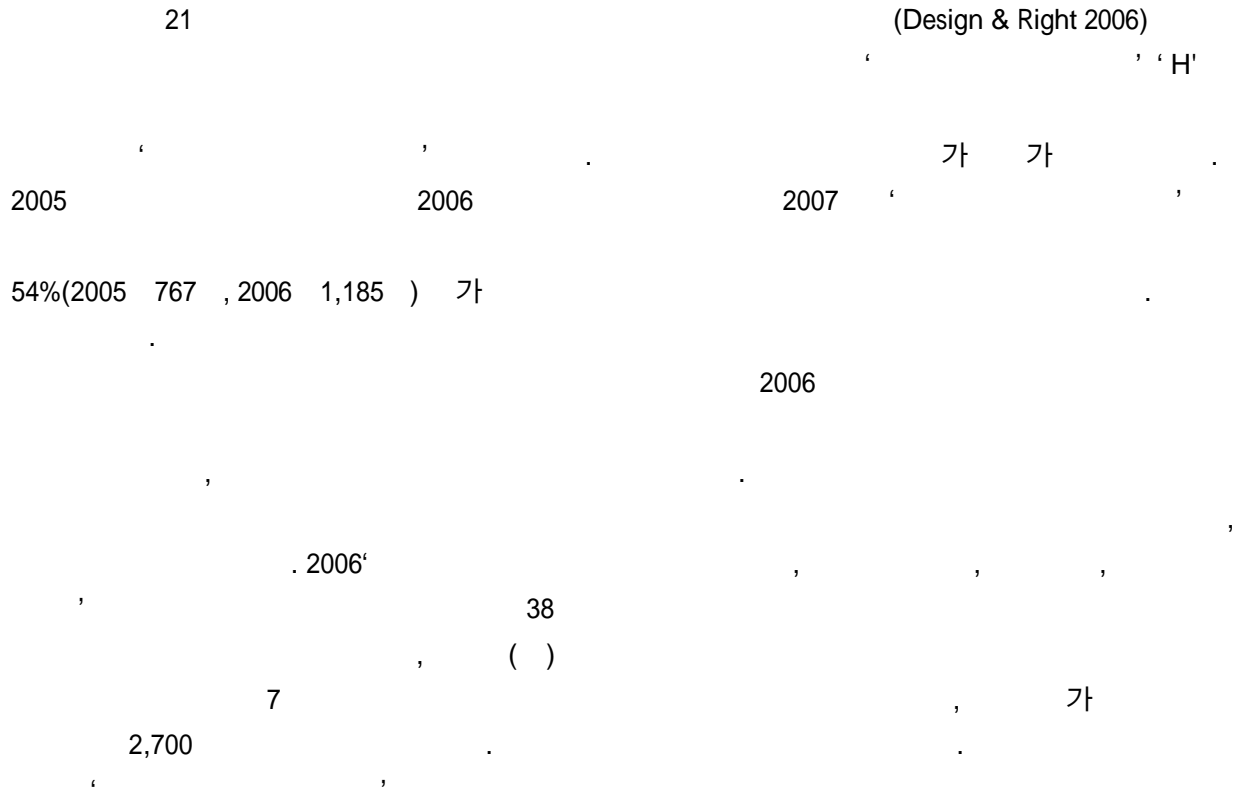
(DDA),

가

가

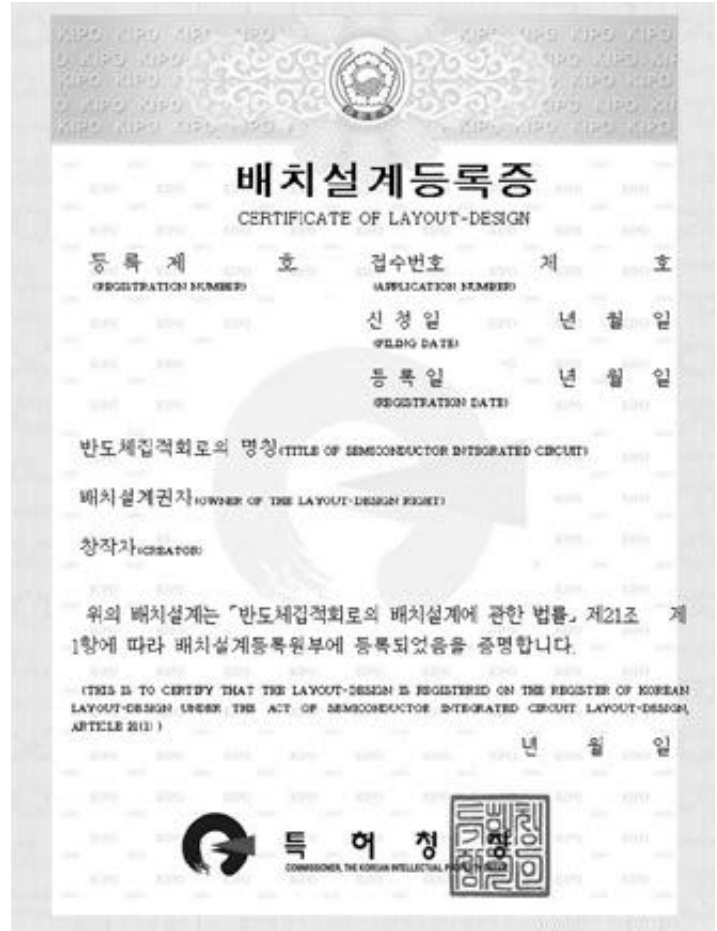


54% 가





30 10 , 5
 11 10
 6 5 , 5
 5
 < . >



	()	
		(06.11.10)
1.	300,000	100,000
2.~9.< >	< >	< >
10.	50,000	
11.~13.< >	< >	< >
14.	10,000	6,500
15.	10,000	500
16.	10,000	500



. ,
 ()
 ,
 ‘ ’
 ‘ ’
 ‘ ’

(NICE) 9 가

, 11.29.
 ()
 가

1957 WIPO()
 “ ”

1998. 3. 1.
 5 2002. 1.
 1. 8 1. 1. 9

가
 가

() 가

