

Differential LC VCO with Enhanced Tank Structure and LC Filtering Techniques in InGaP/GaAs HBT Technology

InGaP/GaAs HBT 공정을 이용하여 향상된 탱크 구조와 LC 필터링 기술을 적용한 차동 LC 전압 제어 발진기 설계

Sang-Yeol Lee · Nam-Young Kim

이 상 열 · 김 남 영

Abstract

This paper presents the InGaP/GaAs HBT differential LC VCO with low phase noise performance for adaptive feedback interference cancellation system(AF-ICS). The VCO is verified with enhanced tank structure including filtering technique. The output tuning range for proposed VCO using asymmetric inductor and symmetric capacitors with low pass filtering technique is 207 MHz. The output powers are -6.68 including balun and cable loss. The phase noise of this VCO at 10 kHz, 100 kHz and 1 MHz are -102.02 dBc/Hz, -112.04 dBc/Hz and -130.40 dBc/Hz. The VCO is designed within total size of 0.9×0.9 mm².

요 약

본 논문은 InGaP/GaAs HBT 공정을 통해 제작한 적응성쇄환 잡음제거시스템용 낮은 위상잡음을 갖는 LC 차동 전압제어 발진기를 제안합니다. 전압제어 발진기는 필터링 기술을 포함한 향상된 공진 탱크 구조를 갖습니다. 비대칭 인덕터 대칭 캐패시터 구조로 제안된 전압제어 발진기의 출력 가변 범위는 207 MHz입니다. 출력 전력은 balun과 케이블 손실을 포함하여 -6.68 dBm입니다. 10 kHz, 100 kHz, 1 MHz에서의 위상잡음은 각각 -102.02 , -112.04 그리고 -130.4 dBc/Hz입니다. 이 전압제어 발진기는 총 0.9×0.9 mm² 면적 내에 집적화되었습니다.

Key words : Symmetrical And Asymmetrical Inductance Tank Structures, Monolithic Microwave Integrated Circuit (MMIC), Heterodyne Bipolar Transistor(HBT), Differential LC VCO, Phase Noise, Cross Coupled Configuration

I. Introduction

To complete the predominant radio frequency communication systems, integrated voltage controlled oscillators(VCOs) are very important parts in every communication system blocks. Nowadays differential topology is one of most common techniques to improve the phase noise of the integrated LC VCOs. Thus, common

building block in RF integrated circuit is differential pair of bipolar transistors^[1].

This differential structure offers high loop gain making it common method to design differential voltage controlled oscillator(VCO) in RFICs. For hybrid VCO design, the traditional Colpitts LC topology is used to avoid the additional complexity that is like in a differential VCO design. For this reason, we could use just

「This research work has been supported by Nano IP/SoC Promotion Group of Seoul R&BD Program in 2006.」

광운대학교 전자공학과 RFIC 센터(RFIC Research and Education Center, Department of Electronic Engineering, Kwangwoon University)

· 논문 번호 : 20061103-10S

· 수정완료일자 : 2007년 1월 29일

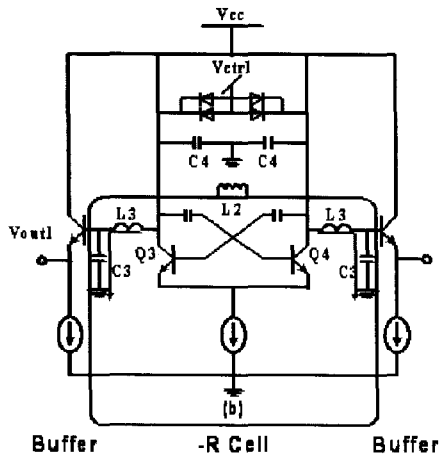


Fig. 3. Circuit schematics of the differential LC VCO proposed SCAIT.

by using cross-coupled capacitance feedback technology^[2].

Phase noise of oscillator is originated primarily through two mechanisms, distinguished by the path into which the noise is injected^[4]. The VCO includes both a feedback oscillation signal path and a frequency control path. This paper treats of the phase noise reduction in these paths using specific frequency filtering technique and enhanced tank structure.

The designed VCO in Fig. 3 consists of asymmetric tank structure. In previous work^[2], the both of inductance and capacitance values are considered asymmetrically. It reduces redundant space that makes comparable performance about conventional symmetric tank structure. But the oscillated wave form of core stage has a little distortion in head room. The problem came from asymmetric capacitor structure in resonator tank. Therefore the distortion and a little loss in headroom are eliminated with enhanced tank using symmetric capacitance in asymmetric tank structure.

The noise frequency filtering structure short noise frequencies around supply voltage and feedback noise to ground.

LC low pass noise frequency filtering technique: To reduce phase noise of the VCO, noise filtering technique is used with LC low pass filter between the core and buffer stages shown as Fig. 3 like a L3 and C3.

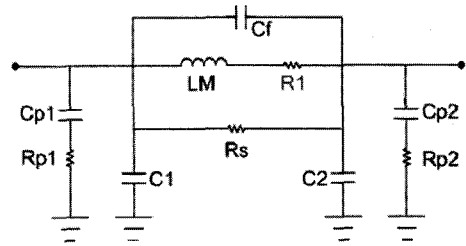


Fig. 4. Equivalent circuit of the spiral inductor.
 LM : Main inductance of the spiral inductor
 C1 : Capacitance of input shunt capacitor
 C2 : Capacitance of output shunt capacitor
 Cf : Series capacitance due to coupling
 R1 : Series resistance of the spiral inductor

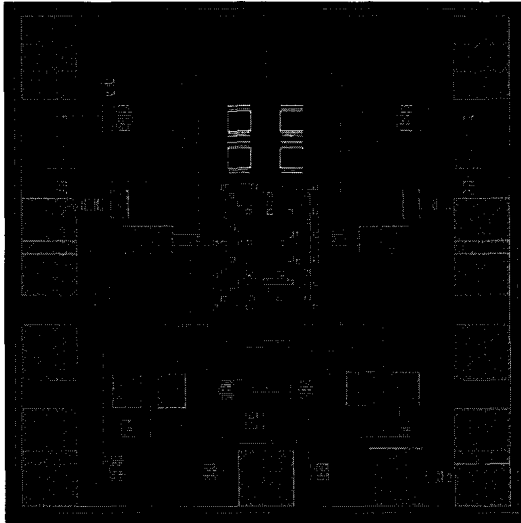
The capacitive filtering technique is based on filtering theory. Because of this VCO design based on active component, which it required to dismiss of other high order harmonics. To remove harmonics, low pass filtering is one of most basic theory. In design, low pass filtered noise frequencies go through ground following the arrow, so enhance phase noise performance can be obtained^[1].

To get a high Q value, the capacitance of the resonator is required to be higher than inductance. So, the inductor needs to be with a low parasitic series resistance. Fig. 4 describes the equivalent circuit of spiral inductor. The R1 that is series resistance of the spiral inductor is dominant factor of the inductor Q-value. Proposed VCO is modified to decrease this series resistance for low phase noise performance.

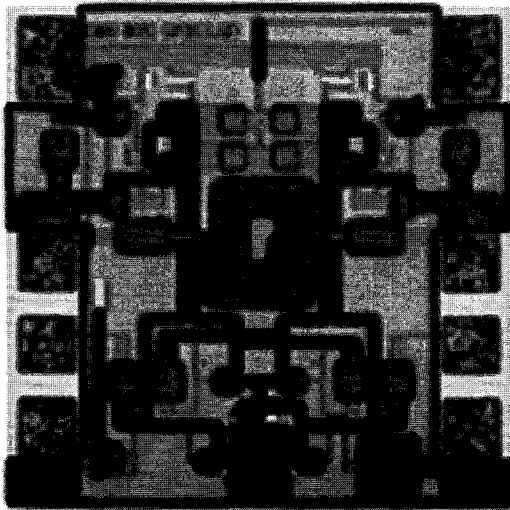
Simulation for this design is based on Agilent Advanced Design System 2004A using Knowledge-on InGaP/GaAs HBT process library. The schematic of this design is verified with checking rule of Knowledge-on process design kit. The outer conditions like a wire bonding inductance and PCB connection loss were considered in final simulation.

III. Measurement Results

The designed VCO with proposed theory is fabricated and measured. The final chip size of VCO is under $1 \times 1 \text{ mm}^2$. This is fabricated by using commercial InGaP/



(a) Layout of 0.9×0.9 mm²



(b) Fabricated chip in of 0.9×0.9 mm²

Fig. 5. Layout and microphotograph of the differential LC VCO.

GaAs HBT process which shows a cut-off frequency(f_T) of 50 GHz and maximum oscillation frequency(f_{MAX}) of 80 GHz. Fig. 4 shows final layout and fabricated chip of SCAIT-VCO design.

The differential output of the SCAIT-VCO is measured using balun to make differential signal as single one like Fig. 6. Output power of fundamental harmonic is -6.68 dBm and difference with second harmonic is 28.44 dBc.

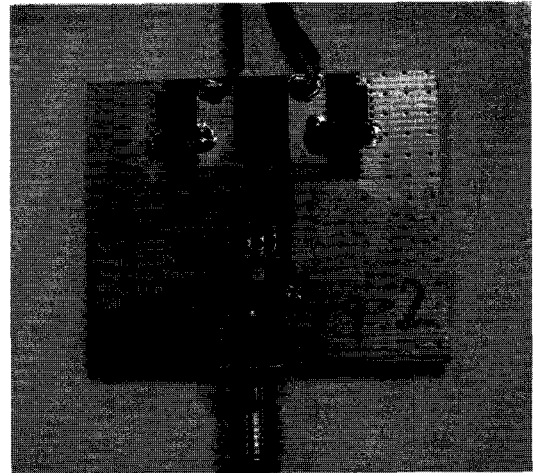


Fig. 6. Top view of VCO test board.

Measured phase noise is -112.04 dBc/Hz at offset frequency 100 kHz. Also, the phase noise at 1 MHz offset is -130.40 dBc/Hz at 1 MHz offset, when supplying control voltage is 0 V. The measured results have a little difference with simulation results. There's a little frequency shifting and decrease of output power. In this LC VCO, enhanced tank with SCAIT structure shows low phase noise performance which is better than previous work^[2].

Table 1 summarizes the measurement results of the proposed LC VCO. Fig. 7 and 8 show output spectrum and phase noise characteristic which are obtained through power spectrum analyzer.

In order to compare the VCO performance with other VCOs in terms of different frequencies and power di-

Table 1. Summary of the measurement results.

Items	Unit	Simulation	Measurement
Oscillation Frequency	GHz	1.624	1.619
Output Power	dBm	-1.945	-6.68
Tuning range(0~3 V)	MHz	138	207
Supply Voltage	V	5	5
Current Consumption	mA	14.8	15
Phase Noise	@100 kHz	dBc/Hz	-109.0
	@ 1 MHz		-129
Chip Size	mm ²	0.9×0.9	0.9×0.9

Table 2. Summary of VCO performance from recently published literature.

Ref.	Type	Freq [GHz]	Tuning Range [MHz]	Phase Noise [dBc/Hz] @100 kHz	Figure of Merit
[5]	CMOS	2.6	-	-99	-177.7
[7]	InGaP HBT	1.46	260	-104	-170.3
[8]	0.5um bipolar	1.9	100	-103	-172.5
[9]	SiGe HBT	2.02	470	-86	-157.3
[10]	SiGe HBT	2.5	120	-107	-168.7
[11]	SiGe HBT	1.476	150	-100	-166.8
[12]	CMOS	1.9	160	-98	-171.8
This work	InGaP HBT	1.619	207	-112.04	-177.5

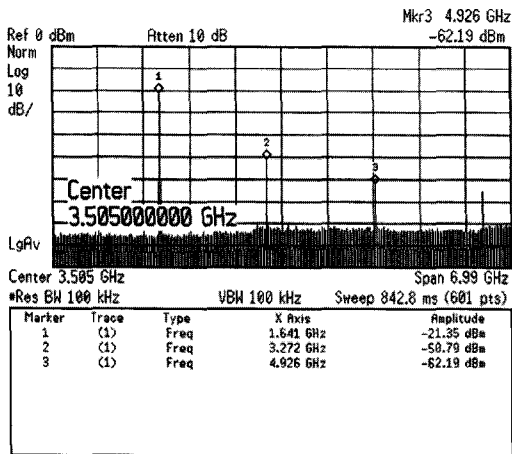


Fig. 7. Output spectrum with harmonic suppression.

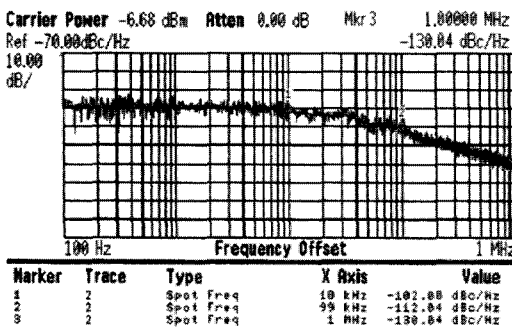


Fig. 8. Phase noise of SCAIT-VCO at low tuning voltage.

ssipations, a widely used figure-of-merit(FOM) is defined as

$$FOM = L(Af_m) - 2 \log\left(\frac{f_0}{f_m}\right) + 10 \log\left(\frac{P_{diss}}{1mW}\right) \quad (1)$$

where L is the measured phase noise, f_0 is the oscillation frequency, f_m is the frequency offset, and P_{diss} is the dissipation power of the VCO. In this work, we achieved a FOM of -177.5 (SCAIT-VCO) which is quite comparable to the previously published one. Table 2 summarized the VCO performance from recently published literature.

IV. Conclusion

In this paper, LC-VCO with noise frequency filtering technique and enhanced tank(symmetric capacitance and asymmetric inductance) is proposed. At this point, the VCO has two kinds of enhanced point to get optimum phase noise characteristic. It has a low phase noise performance base on oscillated signal compensation which is suppressed with symmetric capacitor structure and harmonic noise frequency filtering techniques. Furthermore, the phase noise was obtained using inductor with high Q-factor in LC tank, thereby obtaining wider tuning range than that of the symmetric structure. Also, it has a wider tuning range than the symmetric tank structure. Therefore, such technique can be applied in any other VCO topology to enhance phase noise characteristic.

References

- [1] J. H. Yoon, A. R. Koh, and N. Y. Kim, "A phase noise reduction in differential LC VCO using noise frequency filtering technique", *KEES Microwave and Propagation Fall Conference, IEEE MTT/APMC Korea Chapter*, vol. 28, no. 2, pp. 165-168, Sep. 2005.
- [2] Jae-Ho Yoon, Ah-Rah Koh, and Nam-Young Kim, "Optimized phase noise of LC VCO using an asymmetric inductance tank in InGaP/GaAs HBT tech-

nology", *Microwave and Optical Technology Letters*, Jun. 2006.

[3] S. J. Kim, J. Y. Lee, and N. Y. Kim, "Adaptive feedback interference cancellation system(AF-ICS)", *IEEE MTT-S International Microwave Symposium Digest*, vol. 1, pp. 627-630, Jun. 2003.

[4] Behzad Razavi, *RF Microelectronics*, Prentice Hall PTR, 1998.

[5] Donhee Ham, Ali Hajimiri, "Concepts and methods in optimization of integrated LC VCOs," *IEEE Journal of Solid-State Circuits*, vol. 36, no. 6, pp. 896- 909, Jun. 2001.

[6] Byunghoo Jung, Ramesh Harjani, "High-frequency LC VCO design using capacitive degeneration", *IEEE Journal of Solid-State Circuits*, vol. 39, no. 12, pp. 2359-2370, Dec. 2004.

[7] Choong-Yul Cha, Sang-Gug Lee, "Overcome the phase noise optimization limit of differential LC oscillator with asymmetric capacitance tank structure", *Radio Frequency Integrated Circuits(RFIC) Symposium*, pp. 583-586, Jun. 2004.

[8] John W. M. Rogers, "The effect of varactor non-linearity on the phase noise of completely integrated VCOs", *IEEE Journal of Solid-State Circuits*, vol. 35. no. 9, pp. 1360-1367, Sep. 2000.

[9] Wei-Zen Chen, Jieh-Tsorng Wu, "A 2-V 2-GHz BJT variable frequency oscillator", *IEEE Journal of Solid-State Circuits*, vol. 33, no. 9, pp. 1406-1410, Sep. 1998.

[10] Mihai A. T. Sanduleanu, Jan Peter Frambach, "1 GHz tuning range, low phase noise, LC oscillator with replica biasing common-mode control and quadrature outputs", *ESSCIRC Solid-State Circuits Conference*, pp. 506-509, Sep. 2001.

[11] Leonard Dauphinee, Miles Copeland, "A balanced 1.5 GHz voltage controlled oscillator with an integrated LC resonator", *IEEE Solid-State Circuits Conference*, pp. 390-391, 491, Feb. 1997.

[12] Aleksander Dec, Ken Suyama, "A 1.9 GHz CMOS VCO with micromachined electromechanically tunable capacitors", *IEEE Journal of Solid-State Circuits*, vol. 35, no. 8, pp. 1231-1237, Aug. 2000.

이 상 열



해석

2006년 2월: 광운대학교 전자공학부 (공학사)
 2006년 2월~현재: 광운대학교 전자공학부 석사과정
 [주 관심분야] RFIC/MMIC VCOs, CDMA 무선 시스템 설계, RFID Tag 시스템 및 전기자기적 회로

김 남 영



1987년 2월: 광운대학교 전자공학과 (공학사)
 1989년 8월~1991년 2월: 뉴욕주립대학교(State university of New York at Buffalo) 전자공학과 (공학석사)
 1991년 2월~1994년 2월: 뉴욕주립대학교(State university of New York at Buffalo) 전자공학과 (공학박사)
 1993년 9월~2002년 6월: Midwest college & Teology, M. Div (교육학-박사/신학-석사)
 2000년 9월~2000년 12월: 한양대학교 최고경영자 과정 수료
 2001년 9월~2001년 10월: Stanford 대학 SEIT Program(벤처 비즈니스 과정)수료
 2002년 3월~2002년 9월: 광운대학교 최고경영자 과정 수료
 1998년 2월~현재: 광운대학교 RFIC 센터장
 1994년~현재: 광운대학교 전자공학과 교수
 [주 관심분야] 반도체 소자 모델링(Semiconductor device modeling, ASIC, RFIC 및 MMIC 설계)