

실리콘 웨이퍼 양면 연마 공정의 기구학적 모델링과 해석에 관한 연구

이상직, 정석훈*, 이현섭, 박선준, 김영민, 정해도**
부산대학교 정밀기계공학과*, 부산대학교 기계공학부**

A Study on Kinematical Modeling and Analysis of Double Side Wafer Polishing Process

Sangjik Lee, Sukhoon Jeong*, Hyunseop Lee, Sunjoon Park, Youngmin Kim, and Haedo Jeong**
Dept. of Precision Mechanical Eng. Pusan Nat'l Univ.*, School of Mechanical Eng. Pusan Nat'l Univ.**

Abstract : Double side polishing process has been used for various industrial applications, such as polishing of semiconductor substrates and flat panel display glasses. In wafer manufacturing, double side polishing process is applied to improve wafer flatness and to minimize particle generation from wafers in device manufacturing processes, which is recognized as one of the most important processes. Whereas the kinematical modeling and analysis results of single side polishing, extensively used for chemical-mechanical polishing (CMP) in device manufacturing, are well investigated, the studies in conjunction with double side polishing are barely carried out, due to the complication of polishing system and the uncertainty of wafer motion in the carrier.

This paper suggests the derivation of kinematical model with consideration of carrier and wafer motion in double side polishing, and then presents the effect of kinematical parameters on material removal amount and its non-uniformity. The kinematical analysis results help to understand the double side polishing process and to control the polishing results.

Key Words : Double side polishing, Kinematical modeling, Material removal amount, Non-uniformity