

An investigation of ultra low dielectric constant SiOC(-H) thin films for interconnect technology: Understanding the requirements

R. Navamathavan¹, Heon Ju Lee², Kwang Man Lee³ and Chi Kyu Choi¹

¹Nano Thin Film Materials Laboratory, Department of Physics

²Department of Mechanical, Energy and Production Engineering

³Faculty of Electrical and Electronics Engineering, Cheju National University, Jeju 690-756, Korea

In recent years, the continuous progression of ultra-large scale integration has driven the emergence of technological solutions. In particular, major challenges have been faced for the fabrication of interconnect structures in which the critical problem arises due to continuous miniaturization of interconnects in the form of parasitic capacitance and crosstalk. To mitigate this problem, the interconnect industry urgently demands new materials that have a lower dielectric constant for use in interlayer and intermetal insulation layers. In this paper, we review work that we have performed in our laboratory to understand and characterize these low dielectric constant materials. Various precursors have been used to prepare low dielectric constant SiOC(-H) thin films on p-type Si(100) substrates by using plasma enhanced chemical vapor deposition (PECVD). The characterizations, such as chemical structure, thermal stability, refractive index, mechanical and dielectric constant of the SiOC(-H) films are evaluated. A possible mechanism responsible for the reduction in the dielectric constant of the SiOC(-H) is described. The dielectric constant of the SiOC(-H) film is evaluated by C-V measurements using metal-insulator-semiconductor (MIS), Al/SiOC(-H)/p-Si(100) structure. We also discuss recent process-related advances of low-k interlayer dielectrics in interconnect technology.