Compensation of Power Fluctuations of PV Generation System by SMES Based on Interleaving Technique

Seung-Tak Kim* and Jung-Wook Park†

Abstract – This paper proposes the enhanced application of superconducting magnetic energy storage (SMES) for the effective compensation of power fluctuations based on the interleaving technique. With increases in demand for renewable energy based photovoltaic (PV) generation system, the output power fluctuations from PV generation system due to sudden changes in environmental conditions can cause serious problems such as grid voltage and frequency variations. To solve this problem, the SMES system is applied with its superior characteristics with respect to high power density, fast response for charge and discharge operations, system efficiency, etc. In particular, the compensation capability is effectively improved by the proposed interleaving technique based on its parallel structure. The dynamic performance of the system designed using the proposed method is evaluated with several case studies through time-domain simulations.

Keywords: Distributed power generation, Interleaving technique, Renewable energy system, superconducting magnetic energy storage.

1. Introduction

With increases in demand for renewable energy based distributed generation (DG) systems such as wind and photovoltaic (PV) system, the output power fluctuations from DGs due to sudden changes in environmental conditions can cause serious problems such as grid voltage and frequency variations [1]. To make better use of renewable energy, energy storage devices could be applied to compensate for the power fluctuation.

The energy storage system (ESS) has recently emerged as a more promising device for stabilizing power systems as it has both real and reactive power control abilities. In that sense, flywheel energy storage [2], the electric double layer capacitor (EDLC) [3, 4], and the battery energy storage system (BESS) [5] have been proposed to smooth the power fluctuations. Also, superconducting magnetic energy storage (SMES) is an outstanding power compensator because of the fast-response charging and discharging times and the high energy storage efficiency of the system [6-10].

The interleaving technique is used in various applications with the transistor stages in parallel [11]. The interleaved DC/DC converter topology, as compared with the traditional paralleling of output stage transistors, achieves high efficiency, current distribution, a low ripple reduction, and a small component capacity. In order to have a small input current ripple and zero reverse-recovery loss, a coupling inductor is used [12]. The control method has been studied in order to increase the efficiency during light-load conditions [13]. Also, research has been done to solve the current imbalance of each phase [14].

Therefore, when the interleaving technique is applied to SMES, the ripple current of SMES is reduced [15, 16]. The interleaving technique improves the power quality and the power compensation capabilities. Moreover, the compensation capability is effectively improved by the proposed interleaving technique based on its parallel structure. This paper proposes the enhanced application of SMES for the effective compensation of power fluctuations based on the interleaved technique. To show the effectiveness of the proposed system, its dynamic performance is evaluated using the power systems computer aided design/electromagnetic transients including DC (PSCAD/EMTDC®) based simulation.

2. System Description

The proposed power distribution network is shown in Fig. 1. It consists of a 10 MW grid-connected PV generator and SMES. For rapid response of PV power compensation, the SMES system is directly connected to the DC link of the PV system and shares with the voltage source converter (VSC), because of the slow response of the VSC during the DC power transfer to AC power.

2.1 PV generation system

The PV array is modeled by an equivalent circuit of about 43,000 solar modules, which reflects its non-linear
current-voltage characteristic. In addition, the grid-connected PV system is configured by a multi-string system, where several PV strings are interfaced with their own DC/DC converter to a common inverter. The output characteristics of the PV array always vary with the weather conditions, such as the irradiation levels and atmospheric temperatures. The irradiation profile on a cloudy day is shown in Fig. 2.

Therefore, the maximum power point tracking (MPPT) is important, and the DC-DC converters are used for MPPT, depending on the perturbation and observation (P&O). Under the multi-string structure, each PV string individually tracks the maximum power with high generation efficiency. The VSC transmits real power from the DC-DC converter while maintaining a constant DC link voltage to ensure the reactive power demand of the grid. The VSC control uses the pulse width modulation (PWM) method.

2.2 SMES system

The SMES system consists of a superconducting coil (SC), a DC/DC chopper, and a VSC. The stored energy in the SC, $E_{smes_n}$, is commonly given as a specification, which can be expressed as

$$E_{smes_n} = \frac{1}{2} L_{sc} i_{smes_n}^2$$  \hspace{1cm} (1)

where $L_{sc}$ is the self-inductance of the SC, and $i_{smes_n}$ is the coil current. The SMES system is designed such that it has a maximum current and rated voltage of 1.1 kA and 1 kV, respectively. Table 1 summarizes some parameters of the SMES for simulation.

The charging and discharging of the SMES are accomplished by a DC/DC chopper between the DC link and the SMES coil. Fig. 3 shows in detail the DC/DC chopper topology for SMES operation. The two switching devices, $s_{a,n}$ and $s_{b,n}$, in the DC-DC chopper, are switched on and off simultaneously for the charging and discharging operations [17]. It has a switch for the bypass on standby mode and overvoltage (OV) protection.

The VSC connects a DC link capacitor to the AC network, and it is the same as the VSC of the PV system. On the power generation side, the SMES has an auxiliary role of power compensation. Moreover, the response of the VSC is slower than that of the DC/DC converter due to the DC power transfer to AC power. Therefore, the proposed SMES system is directly connected to the DC link of the PV system and shares with the VSC of the PV system.

The proposed interleaved technique is applied to the SMES and PV generation systems in distribution power network as shown in Fig. 1. The interleaving technique is simply achieved by the PWM carrier signals of all of the converters in parallel operating out of phase. Because the current ripples of the SMES are out of phase, they tend to cancel each other and reduce the current ripple caused by the SMES. In this paper, the proposed interleaved SMES system uses six ($N=6$) SCs and DC/DC choppers.
3. Control of SMES Converter

3.1 Compensation of power fluctuation

The control block diagram of the proposed interleaved SMES system is shown in Fig. 4. Compensation of the output power fluctuations is the primary control object and it requires fast response. The reference power that is compensated by the SMES, $P_{smes\_ref}$, can be computed by the 3rd order high pass Butterworth filter, as expressed in

$$P_{smes\_ref} = \frac{s^3}{s^3 + 2\omega_c s^2 + 2\omega^2 s + \omega^3} P_{pv}$$  \hspace{1cm} (2)

where $P_{pv}$ is the PV generation power and $\omega_c$ is the cut-off frequency. In this study, the time constant, which is related to the cutoff frequency, is set to be 334 s. The degree of smoothing can be modified by adjusting the cut-off frequency.

3.2 Energy maintenance of the SC

Compensation of the output power fluctuations operates according to the high pass filtering, which removes the DC component. The remaining energy after compensating the fluctuation for one day is the same as the previous charged energy. Due to the small losses in the SMES and the DC-DC chopper circuit, and especially the VSC control reference error by affecting the SMES, the remaining energy after compensation for one day can be changed. Therefore, it is necessary to maintain the energy of the SMES. The energy reference, $E_{smes\_ref}$, is about 67 %, and the SC current is 0.9 kA. Note that the maintenance control response of the SMES energy must be slower than the compensation control response of the power fluctuations.

3.3 Balancing of SMES current

Interleaved system requires a current loop in each phase to control the dc current per phase. The current balancing between interleaved phases is essential. While an unbalanced current causes more thermal stress in one phase, it is necessary in order to avoid the overheating of some components and to prevent saturation [18]. Therefore, the current control loop with the balancing current reference, $i_{smes\_avr}$, which is the average value of each SC current, is used for interleaved controlling the phase individually.

3.4 Switching signal generation

The power reference for charging and discharging is normalized by the DC link voltage, $v_{smes}$, and the average current reference, $i_{smes\_avr}$. Then the modulating signal of the SMES per interleaved phase, $m_n$, is calculated with the sum of the normalized reference and the current balancing reference. The carrier signal is a saw wave from $-1$ to $1$. When the modulating signal is larger than the carrier signal, a turn-on command is issued to $s_a$ and $s_b$. Once the modulating signal is smaller than the carrier signal, $s_a$ and $s_b$ are switched off. Changing the modulating signal can control the current of the SC as follows [19]

$$\frac{di_{smes\_n}}{dt} = \frac{1}{L_{sc\_n}} v_{sc\_n} = \frac{1}{L_{sc\_n}} m_n v_{smes}$$  \hspace{1cm} (3)
where \( V_{sc,n} \) is the averaged voltage of the SC during the one switching period. Therefore, when the sign of the modulating signal is positive, the SMES is charged, and if the sign of the modulating signal is negative, the SMES operates in discharging mode.

4. Results and Analysis

The performance of the proposed method is evaluated through a simulation based on the PSCAD/EMTDC® software, and the system is run with a 10 \( \mu s \) time-step from 6 s to 18 s. The simulation results are shown in Fig. 5 and Fig. 6.

To evaluate the performance of the proposed SMES system, a case study of several types of SMES systems is carried out. The conventional SMES system consists of one SC and a DC/DC chopper. The parallel SMES system has the same configuration as the proposed SMES, but the interleaved control method is not applied.

As shown in Fig. 5, all SMES systems operate while compensating for power fluctuations. Nevertheless, the proposed system is comparable with others. During the time from 12.5 to 13 s, the conventional system has power varying from 6.11 MW to 8.75 MW, while the PV power changes from 5.3 MW to 10.12 MW as shown in Fig. 5(b).

The power of the parallel SMES system varies from 7.04 MW to 8.62 MW, and the proposed system has power fluctuations from 7.38 MW to 8.7 MW. The conventional system is insufficient for maximum fluctuation compensation power, \( P_{smes\_max} \), which can be expressed as

\[
P_{smes\_max} = \sum_{n=1}^{N} V_{smes} \cdot i_{smes\_n}
\]  

The comparisons of the parallel SMES system and the proposed SMES system are shown in Fig. 6. The output power of the parallel SMES system for PV power compensation is larger than the power of the proposed system as shown in Fig. 6(a).

Moreover, in the case of parallel SMES system, the current of the SC at 6 s is significantly different than that at 18 s as shown in Fig. 6(b), and the difference of interleaving current with parallel method current is shown in Fig. 6(c). The reason of these problem is the VSC control reference error according to the voltage fluctuation of the DC link capacitor by affecting the ripple current of the SMES charging and discharging operation. Fig. 6(d) shows the voltage of the DC link capacitor.

In addition, the proposed SMES system improves the power quality. Table 2 shows the total harmonic distortion (THD) of the grid voltage. Because the DC link voltage is stabilized by the proposed method, the VSC control performance is also improved.

<table>
<thead>
<tr>
<th>System</th>
<th>THD</th>
</tr>
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<tbody>
<tr>
<td>Conventional</td>
<td>1.367</td>
</tr>
<tr>
<td>Parallel</td>
<td>2.867</td>
</tr>
<tr>
<td>Proposed</td>
<td>1.318</td>
</tr>
</tbody>
</table>

![Fig. 6. Simulation results of SMES system: (a) output power of SMES; (b) average current of SC, (c) current of SC, and (d) voltage of DC link capacitor.](image-url)
5. Conclusion

This paper proposes the enhanced application of SMES for the effective compensation of power fluctuations based on the interleaving technique. The SMES system and the control method based on its parallel structure is proposed to improve the compensation capability and power quality. The dynamic performance of the system designed using the proposed method is evaluated with several case studies by time-domain simulations. The simulation results show that the proposed SMES system is able to enhance both compensation capability and power quality.

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References

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Seung-Tak Kim received the B.S. and Ph.D. degrees from the School of Electrical and Electronic Engineering, Yonsei University, Seoul, Korea, in 2009 and 2014, respectively. He is currently a Lead Researcher in the Hyundai Heavy Industries Co. Ltd, Yongin-si, Gyeonggi-do, Korea. His research interests are in control of renewable energies based systems, hardware implementation of grid-connected inverter with photovoltaic and energy storage devices, and energy management system for stabilization of distributed generation systems.

Jung-Wook Park was born in Seoul, Korea. He received the B.S. degree (summa cum laude) from the Department of Electrical Engineering, Yonsei University, Seoul, Korea, in 1999, and the M.S.E.E and Ph.D. degrees from the School of Electrical and Computer Engineering, Georgia Institute of Technology, Atlanta, USA in 2000 and 2003, respectively. He was a Post-doctoral Research Associate in the Department of Electrical and Computer Engineering, University of Wisconsin, Madison, USA during 2003-2004, and a Senior Research Engineer with LG Electronics Inc., Korea during 2004-2005. He is currently an Associate Professor in the School of Electrical and Computer Engineering, Yonsei University, Seoul, Korea. He is now leading the National Leading Research Laboratory (NLRL) designated by Korea government to the subject of integrated optimal operation for smart grid. His current research interests are in power system dynamics, renewable energies based distributed generations, power control of electric vehicle, and optimization control algorithms. Prof. Park was the recipient of the Young Scientist Presidential Award in 2013 from the Korean Academy of Science and Technology (KAST), Korea.